

Abstract

Digital Signal Delay Device

The invention relates to a digital signal delay device (101) for converting a signal (IN) into a corresponding delayed signal (OUT), comprising a plurality of signal delay elements (103a, 103b, 103c) connected in series, wherein, as a function of the desired delay of the delayed signal (OUT), the output signal of a particular signal delay element (103a, 103b, 103c) is used for generating the delayed signal (OUT), and wherein the signal delay elements (103a, 103b, 103c) each comprise one single inverter (105, 106, 107) only.